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PATENT
8070-1005

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Akira YOSHINO

Conf. 7064

Application No. 10/716,703

Group 2818

Filed November 20, 2003

Examiner Tu Tu V. HO

NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR
MANUFACTURING SAME

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the cited documents are made of record on the enclosed PTO Form-1449.

As the USPTO has waived the requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and patent publication for applications filed after June 30, 2003, copies of the cited U.S. references are not enclosed, as the present application is filed after June 30, 2003. Copies of the cited foreign patent documents and/or non-patent literature are enclosed.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in the corresponding Japanese Application Serial No. 2002-357171, filed December 9, 2002. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion en-

closed in a wavy line. **An English translation of the enclosed portion is also attached hereto.**

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this Statement.

Respectfully submitted,

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* English language abstract provided for the Examiner's convenience